

## Ce Yang - Curriculum Vitae

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### ACADEMIC EDUCATION

- 2016 – present      **Ph.D.EE.** of mixed-signal circuit design in Electrical Engineering  
University of Southern California (USC), Los Angeles, California, US
- 2012 – 2016        **B.S.E.E.**  
Peking University (PKU), Beijing, China

### MAIN AREA OF RESEARCH

**Research Assistant**, Dept. of EE-Electrophysics, USC. Advisor: Prof. Mike Shuo-Wei Chen

- Non-Uniform Time-Approximation-Filter Receiver. (2020-present)
- Non-uniform subsampling receiver front-end using spectral alias spreading for alias rejection. (2017-2019)

### PEER REVIEWED PUBLICATIONS

[1] A. Zhang, **C. Yang**, M. Ayesh and M. S. -W. Chen, "26.6 A 5-to-6GHz Current-Mode Subharmonic Switching Digital Power Amplifier for Enhancing Power Back-Off Efficiency," *2021 IEEE International Solid-State Circuits Conference (ISSCC)*, 2021, pp. 364-366.

[2] **Ce Yang**, Mostafa Ayesh, Aoyang Zhang and Mike Shuo-Wei Chen, "A 29-mW 26.88-GHz Non-Uniform Sub-Sampling Receiver Front-End Enabling Spectral Alias Spreading," *IEEE Radio Frequency Integrated Circuits Symposium (RFIC)*, pp. 87-90, 2020.

[3] V. Ongun Özçelik, Javad G. Azadani, **Ce Yang**, Steven J. Koester, and Tony Low, "Band alignment of two-dimensional semiconductors for designing heterostructures with momentum space matching," *Phys. Rev. B* 94, 035125, July 2016.

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